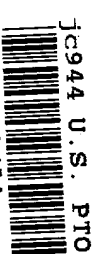


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J1050 U.S. PTO

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Jian Li et al.

Title: STACKED FERROELECTRIC MEMORY DEVICE AND METHOD OF MAKING SAME

Attorney Docket No.: 884.562US1



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09/960125

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PATENT APPLICATION TRANSMITTAL**BOX PATENT APPLICATION**

Commissioner for Patents

Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

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CONTINUATION-IN-PART (CIP) of prior Patent Application No. 09/909,670 (under 37 CFR § 1.53(b))

comprising:

☒ Specification (33 pgs, including claims numbered 1 through 29 and a 1 page Abstract).☒ Formal Drawing(s) (13 sheets).☒ **Signed Combined Declaration and Power of Attorney** (6 pgs).☒ Check in the amount of \$1,032.00 to pay the filing fee.☒ Assignment of the invention to Intel Corporation (4 pgs) and Recordation Form Cover Sheet.☒ Check in the amount of \$40.00 to pay the Assignment recording fee.☒ Information Disclosure Statement (1 pgs), Form 1449 (1 pgs), and copies of cited references (9).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	29 - 20 =	9	x 18 =	\$162.00
INDEPENDENT CLAIMS	5 - 3 =	2	x 80 =	\$160.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$710.00
TOTAL				\$1,032.00

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This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.